Revised June 1999

# ■ Wide supply voltage range: 3.0V to 15V

- High noise immunity: 0.45 V<sub>DD</sub> (typ)
- Low power TTL compatibility:

**Features** 

- fan out of 2 driving 74L or 1 driving 74LS ■ 5V–10V–15V parametric ratings
- Symmetrical output characteristics
- Maximum input leakage 1 µA at 15V over full temperature range

#### **Ordering Code:**

FAIRCHILD

**CD4023BC** 

SEMICONDUCTOR

**General Description** 

charge with diodes to V<sub>DD</sub> and V<sub>SS</sub>.

Order Number	Package Number	Package Description
CD4023BCM	M14B	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide
CD4023BCS	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
CD4023BCN	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide
Devices also available	in Tane and Real Specify	y by appending the suffix letter "X" tot be ordering code

**Buffered Triple 3-Input NAND Gate** 

These triple gates are monolithic complementary MOS

(CMOS) integrated circuits constructed with N- and P-

channel enhancement mode transistors. They have equal

source and sink current capabilities and conform to stan-

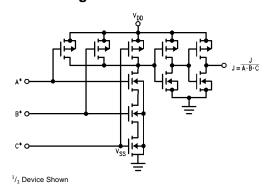
dard B series output drive. The devices also have buffered outputs which improve transfer characteristics by providing

very high gain. All inputs are protected against static dis-

**Connection Diagram** ٧<sub>SS</sub>

Top View

#### **Block Diagram**



\*All Inputs Protected by Standard CMOS Input Protection Circuit.

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#### Absolute Maximum Ratings(Note 1) (Note 2)

DC Supply Voltage (V <sub>DD</sub> )	–0.5 $V_{DC}$ to +18 $V_{DC}$
Input Voltage (V <sub>IN</sub> )	–0.5 $V_{\text{DC}}$ to $V_{\text{DD}}\text{+}0.5$ $V_{\text{DC}}$
Storage Temp. Range (T <sub>S</sub> )	$-65^{\circ}C$ to $+150^{\circ}C$
Power Dissipation (P <sub>D</sub> )	
Dual-In-Line	700 mW
Small Outline	500 mW
Lead Temperature (T <sub>L</sub> )	
(Soldering, 10 seconds)	260°C

## Recommended Operating Conditions

DC Supply Voltage (V<sub>DD</sub>) Input Voltage (V<sub>IN</sub>) Operating Temperature Range (T<sub>A</sub>)

 $\begin{array}{c} 5 \ V_{DC} \ to \ 15 \ V_{DC} \\ 0 \ V_{DC} \ to \ V_{DD} \ V_{DC} \\ -40^\circ C \ to \ +85^\circ C \end{array}$ 

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed; they are not meant to imply that the devices should be operated at these limits. The table of "Recommended Operating Conditions" and "Electrical Characteristics" provides conditions for actual device operation.

Note 2:  $V_{SS} = 0V$  unless otherwise specified.

### DC Electrical Characteristics (Note 3)

Symbol	Parameter	Conditions	Conditions -40°C +25°C +85°C	5°C Unit						
Symbol	Falameter	Conditions	Min	Тур	Min	Тур	Max	Min	Max	Units
I <sub>DD</sub>	Quiescent Device Current	$V_{DD} = 5V$		1.0		0.004	1.0		7.5	μA
		$V_{DD} = 10V$		2.0		0.005	2.0		15	
		$V_{DD} = 15V$		4.0		0.006	4.0		30	
V <sub>OL</sub>	LOW Level Output Voltage	$V_{DD} = 5V$		0.05		0	0.05		0.05	V
		$V_{DD} = 10V$		0.05		0	0.05		0.05	
		$V_{DD} = 15V$		0.05		0	0.05		0.05	
V <sub>OH</sub>	HIGH Level Output Voltage	$V_{DD} = 5V$	4.95		4.95	5		4.95		V
		$V_{DD} = 10V$	9.95		9.95	10		9.95		
		$V_{DD} = 15V$	14.95		14.95	15		14.95		
V <sub>IL</sub>	LOW Level Input Voltage	V <sub>DD</sub> =5V, V <sub>O</sub> =4.5V		1.5		2	1.5		1.5	V
		V <sub>DD</sub> =10V, V <sub>O</sub> =9.0V  I <sub>O</sub>  <1µA		3.0		4	3.0		3.0	
		V <sub>DD</sub> =15V, V <sub>O</sub> =13.5V		4.0		6	4.0		4.0	
V <sub>IH</sub>	HIGH Level Input Voltage	V <sub>DD</sub> =5V, V <sub>O</sub> =0.5V	3.5		3.5	3		3.5		V
		V <sub>DD</sub> =10V, V <sub>O</sub> =1.0V  I <sub>O</sub>  <1µA	7.0		7.0	6		7.0		
		V <sub>DD</sub> =15V, V <sub>O</sub> =1.5V	11.0		11.0	9		11.0		
I <sub>OL</sub>	LOW Level Output Current	$V_{DD}=5V, V_{O}=0.4V$	0.52		0.44	0.88		0.36		mA
	(Note 4)	$V_{DD} = 10V, V_{O} = 0.5V$	1.3		1.1	2.2		0.90		
		$V_{DD} = 15V, V_{O} = 1.5V$	3.6		3.0	8		2.4		
I <sub>ОН</sub>	HIGH Level Output Current	$V_{DD} = 5V, V_{O} = 4.6V$	-0.52		-0.44	-0.88		-0.36		mA
	(Note 4)	$V_{DD} = 10V, V_{O} = 9.5V$	-1.3		-1.1	-2.2		-0.90		
		$V_{DD} = 15V, V_{O} = 13.5V$	-3.6		-3.0	-8		-2.4		
I <sub>IN</sub>	Input Current	$V_{DD} = 15V, V_{IN} = 0V$		-0.3		-10 <sup>-5</sup>	-0.3		-1.0	μΑ
		$V_{DD} = 15V, V_{IN} = 15V$		0.3		10 <sup>-5</sup>	0.3		1.0	

Note 3:  $V_{SS} = 0V$  unless otherwise specified.

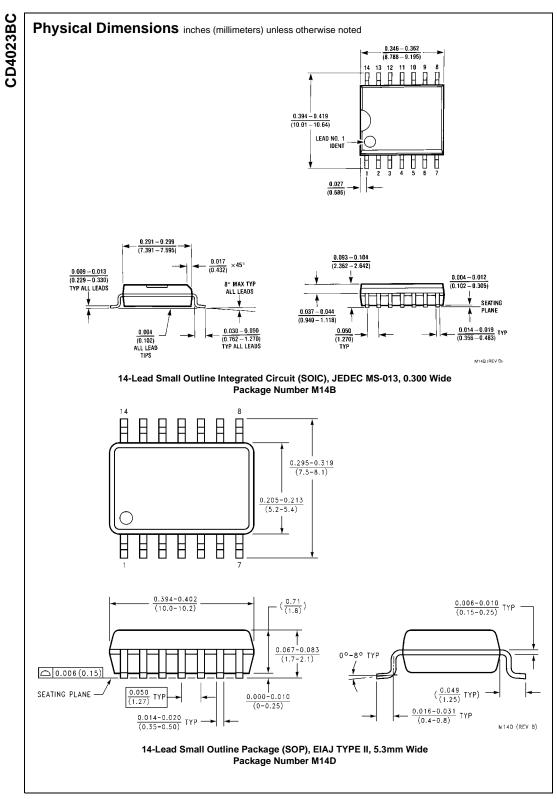
Note 4:  $I_{OH}$  and  $I_{OL}$  are tested one output at a time.

T <sub>A</sub> = 25°C, C <sub>L</sub> = 50 pF, R <sub>L</sub> = 200k, unless otherwise specified   Symbol Parameter Conditions Min Typ Max Units							
Symbol			IVIIII	Тур		Units	
t <sub>PHL</sub> P	Propagation Delay, HIGH-to-LOW Level	$V_{DD} = 5V$		130	250	ns	
		$V_{DD} = 10V$		60	100		
		$V_{DD} = 15V$		40	70		
t <sub>PLH</sub> F	Propagation Delay, LOW-to-HIGH Level	$V_{DD} = 5V$		110	250	ns	
		$V_{DD} = 10V$		50	100		
		$V_{DD} = 15V$		35	70		
t <sub>THL</sub> ,	Transition Time	$V_{DD} = 5V$		90	200	ns	
t <sub>TLH</sub>		$V_{DD} = 10V$		50	100		
		$V_{DD} = 15V$		40	80		
C <sub>IN</sub>	Average Input Capacitance	Any Input		5	7.5	pF	
CPD	Power Dissipation Capacity (Note 6)	Any Gate		17		pF	

Note 5: AC Parameters are guaranteed by DC correlated testing.

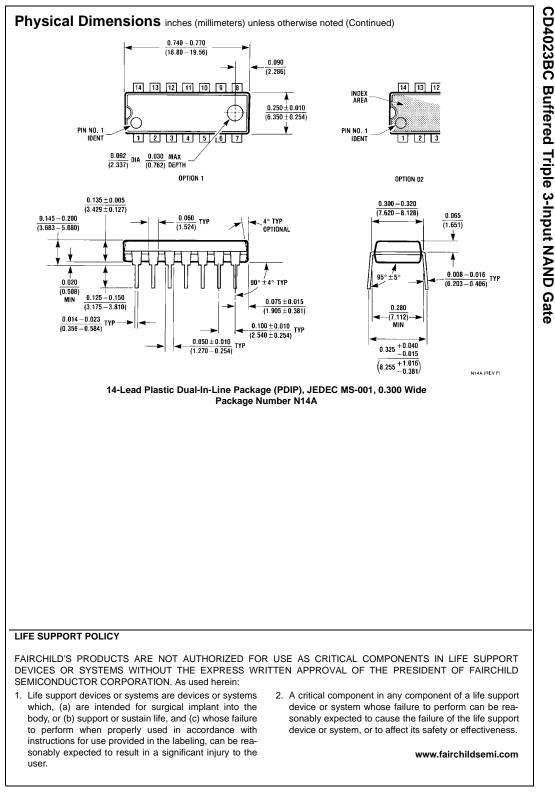
**Note 6:** C<sub>PD</sub> determines the no load AC power consumption of any CMOS device. For complete explanation, see Family Characteristics Application Note AN-90.

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4



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